

APPENIX D

KEYBOARD MATRIX

912C/920C

Below is the matrix ("schematic") that is expected on the keyboard attached to connector P1. The character interpretation is actually a firmware decision.

COLUMN		#0	#1	#2	#3	#4	#5	#6	#7
PIN#		8	9	10	11	12	13	14	15
ROW #0	20	N	\$	R	G	LINE FEED	BLOCK CONV		
#1	16	X	ESC	TAB	A	I	L	-	Z
#2	21	M	5	T	H			8	
#3	17	C	1	Q	S	O	:	+	CLEAR
#4	22	<	6	Y	J	'	?	(
#5	18	V	2	W	D	P	ENTER	0	DEL
#6	23	.	7	U	K	BERAK	SPACE BAR	BACK SPACE	HOME
#7	19	B	3	E	F	[RETURN	~	}
#8	26					BACK TAB	SEND PAGE	SEND LINE	PAGE ERASE
#9	25	LINE ERA	LINE DEL	LINE INS	CHAR DEL	CHAR INS	F11	F10	F9
#10	24	F8	F7	F6	F5	F4	F3	F2	F1
#11	7								

ALPHA LOCK	3
SHIFT	4
FUNCT	5
CTRL	6
GROUND	1,2

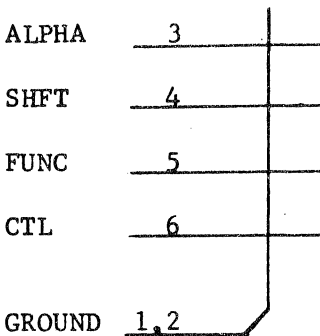
APPENDIX D

KEYBOARD MATRIX

912B/920B

Below is the matrix ("schematic") that is expected on the keyboard attached to connector P1. The character interpretation is actually a firmware decision.

COLUMN	PIN #	#0	#1	#2	#3	#4	#5	#6	#7
	→	8	9	10	11	12	13	14	15
ROW #0	20	N	4	R	G	CR	PAGE]	→
#1	16	X	TAB	ESC	A	I	L	:	Z
#2	21	M	5	T	H	PRINT	←	8	↑
#3	17	C	1	Q	S	O	:	-	CLEAR
#4	22	,	6	Y	J	RUB	/	9	↓
#5	18	V	2	W	D	P	@	0	CLR TAB
#6	23	.	7	U	K	BREAK	SP	^	HOME
#7	19	B	3	E	F	LF	\	[PROT
#8	26								
#9	25					BRK TAB	SND PAI	SD LIN	PG ERA
#10	24	LIN ERA	LIN DEL	LIN INS	CHR DEL	CHR INS	F11	F10	F9
#11	7	F8	F7	F6	F5	F4	F3	F2	F1



APPENDIX E

CONNECTOR LISTS

P1	KEYBOARD CONNECTOR (See also Appendix D)		
PIN 1	Ground for Keyboard		
2	"	"	"
3	Input from keyboard	ALPHA KEY	Bit 6 of 40C to 40F
4	"	"	" SHFT KEY " 4 " " "
5	"	"	" FUNC KEY " 2 " " "
6	"	"	" CTL KEY " 3 " " "
7	Input from keyboard	Matrix	Bit 5 of 40F
8	Output to keyboard	Matrix Column	Bit 0 of Port 1
9	Output to keyboard	Matrix Column	Bit 1 of Port 1
10	Output to keyboard	Matrix Column	Bit 2 of Port 1
11	Output to keyboard	Matrix Column	Bit 3 of Port 1
12	Output to keyboard	Matrix Column	Bit 4 of Port 1
13	Output to keyboard	Matrix Column	Bit 5 of Port 1
14	Output to keyboard	Matrix Column	Bit 6 of Port 1
15	Output to keyboard	Matrix Column	Bit 7 of Port 1
16	Input from keyboard	Matrix	Bit 1 of 40C
17	Input from keyboard	Matrix	Bit 1 of 40D
18	Input from keyboard	Matrix	Bit 1 of 40E
19	Input from keyboard	Matrix	Bit 1 of 40F
20	Input from keyboard	Matrix	Bit 0 of 40C
21	Input from keyboard	Matrix	Bit 0 of 40D
22	Input from keyboard	Matrix	Bit 0 of 40E
23	Input from keyboard	Matrix	Bit 0 of 40F
24	Input from keyboard	Matrix	Bit 5 of 40E

P1 KEYBOARD CONNECTOR (Continued)

- 25 Input from keyboard Matrix Bit 5 of 40D
- 26 Input from keyboard Matrix Bit 5 of 40C

P2 VIDEO CONNECTOR

- PIN 1 +HSYNC
- 2 "KEY"
- 3 VIDEO SHIELD GROUND
- 4 +VIDEO
- 5 -VSYNC
- 6 +TTL VIDEO (or -COMPSYNC with S2-10, 11 open)

P3 COMPUTER PORT

- PIN 1
- 2 TXD (RS232) Transmit Data, Output
- 3 RCVD (RS232) Receive Data, Input
- 4 RTS (RS232) Request to Send, Output
- 5 CTS (RS232) Clear to Send, Input
- 6 DCR2 (RS232) "Data Carrier Ready, (S5-2, 13), Input
- 7 GROUND
- 8 DCR1 (RS232) "Data Carrier Ready" (S5-1, 14), Input
- 9
- 10
- 11
- 12 RXD1 (TTY) Current Loop Receive Data 1, Input
- 13 TXD2 (TTY) Current Loop Transmit Data 2, (Return) Output
- 14
- 15
- 16
- 17

P3 COMPUTER PORT (Continued)

PIN 18

19

20 DTR (RS232) Data Terminal Ready (S5-3, 12 and S5-4,11), Output

21

22

23

24 RXD2 (TTY) Current Loop Receive Data 2 (Returns) Input

25 TXD1 (TTY) Current Loop Transmit Data 1, Output

P4 PRINTER PORT

PIN 1

2

3 PRT DATA (RS232) Transmit Data, Output

4

5

6 TERM RDY2 (RS232) "Terminal Ready" (W12), Output

7 GROUND

8 TERM RDY1 (RS232) "Terminal Ready" (W13), Output

9

10

11

12

13

14

15

16

17

P4 PRINTER PORT (Continued)

PIN 18

19

20 +PRTRDY (RS232) Printer Ready, Input

21

22

23

24

25

P5 POWER SUPPLY CONNECTOR

PIN 1 -12V

2 "KEY"

3 GROUND

4 +5V

5 +12V

P7

PIN 1 Speaker 8 Ω

2 " "